

## AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application.

### **Listing of Claims:**

1. (currently amended) A frequency harmonic identifier system for a circuit interrupter, the system comprising:
  - a frequency analyzer for receiving an input signal and providing an output representing a tested signal;
  - a storage area containing at least one reference signal band; and,
  - a decision logic, wherein the decision logic compares harmonic content of selected bands of the tested signal to harmonic content of each of the at least one reference signal band for determining if the tested signal is a series arc signal or a load, thereby providing for frequency harmonic identification of the series arc signal.
2. (currently amended) The system of claim 1 wherein the frequency analyzer provides a Fast Fourier Transform of the input signal and a band selector for selecting bands of the Fast Fourier Transform of the input signal for comparing to the at least one reference signal band.
3. (original) The system of claim 1 wherein the input signal is a sensed current on a power line.
4. (original) The system of claim 1 wherein the storage area contains a plurality of sets of reference signal bands.

5. (currently amended) The system of claim 4 wherein the plurality of sets of reference signal bands are classified according to highest magnitude harmonics within frequency bands.

6. (original) The system of claim 1 wherein the at least one reference signal band represents at least one common load.

7. (canceled)

8. (currently amended) The system of claim 1 wherein a circuit interrupt decision is made based on the decision logic comparison, wherein, if the tested signal is determined to be a series arc signal by the decision logic, then a trip signal is provided ~~sent from the frequency harmonic identifier~~.

9. (currently amended) A circuit interrupter with series arc detection capability, the circuit interrupter comprising:

a pair of separable contacts for interrupting a circuit;

a frequency harmonic identifier system having:

a frequency analyzer for receiving an input signal and providing an output representing a tested signal;

a storage area containing at least one reference signal band; and,

a decision logic, wherein the decision logic compares harmonic content of the tested signal to harmonic content of each of the at least one reference signal band for determining if the tested signal is a series arc signal or a load, thereby providing for frequency harmonic identification of the series arc signal;

wherein a trip signal is provided ~~sent from the frequency harmonic identifier~~ if the tested signal is determined by the decision logic to be a series arc signal, and wherein the pair of separable contacts are separated after issuance of the trip signal.

10. (original) The circuit interrupter of claim 9 further comprising at least one of a current sensor and a shunt resistor for sensing current on the circuit for use as the input signal.

11. (currently amended) The circuit interrupter of claim 9 wherein the frequency harmonic identifier system is provided within a microprocessor within the circuit interrupter.

12. (original) The circuit interrupter of claim 9 wherein the storage area is a memory accessible by the decision logic.

13. (currently amended) The circuit interrupter of claim 9 wherein the frequency analyzer provides a Fast Fourier Transform of the input signal and a band selector for selecting bands of the Fast Fourier Transform of the input signal for comparing to the at least one reference signal band.

14. (currently amended) The circuit interrupter of claim 9 wherein the storage area contains a plurality of sets of reference signal bands classified according to highest magnitude harmonics within frequency bands and wherein ~~the~~ at least one of the plurality sets of reference signal bands represents at least one common load.

15. (canceled)

16. (currently amended) A method for detecting series arcs in a circuit interrupter, the method comprising:

sensing current on a power line and providing a sensed current signal as an input signal to a frequency harmonic identifier;

performing a Fast Fourier Transform on the input signal and selecting bands of the Fast Fourier Transform of the input signal for providing tested signal bands;

accessing a storage area storing at least one reference signal band;  
comparing harmonic content of the tested signal bands to harmonic content of  
each of the at least one reference signal band and determining if the input signal is a  
series arc signal through the comparison; and,  
if the input signal is a series arc signal, sending a trip signal.

17. (original) The method of claim 16 wherein the at least one reference signal  
band is a plurality of sets of reference signal bands representing a plurality of common  
loads.

18. (original) The method of claim 17 wherein determining if the input signal is a  
series arc signal comprises comparing harmonic content of the tested signal bands to  
harmonic content of each of the plurality of sets of reference signal bands.

19. (currently amended) A storage medium encoded with machine-readable  
computer program code executable by a computer for detecting series arcs on a power  
line, the storage medium including instructions for execution by the ~~causing a~~ computer  
to implement a method comprising:

sensing current on a power line and providing a sensed current signal as an input  
signal to a frequency harmonic identifier;

performing a Fast Fourier Transform on the input signal and selecting bands of the  
Fast Fourier Transform of the input signal for providing tested signal bands;

accessing a storage area storing at least one reference signal band;

comparing harmonic content of the tested signal bands to harmonic content of the  
at least one reference signal band and determining if the input signals is a series arc signal  
through the comparison; and,

if the input signal is a series arc signal, sending a trip signal.

20. (currently amended) The storage medium of claim 19 further comprising instructions for causing the [[a]] computer to implement:

comparing harmonic content of the tested signal bands to harmonic content of each of the at least one reference signal band.